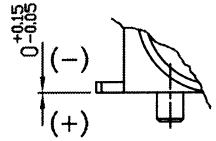
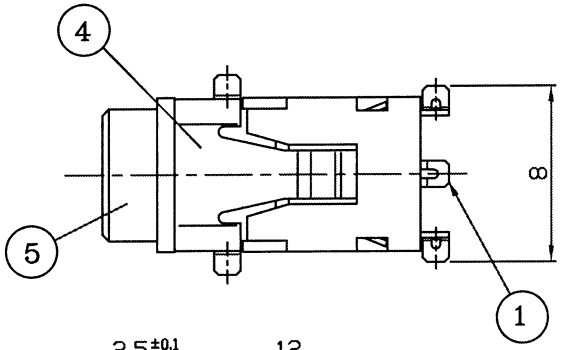
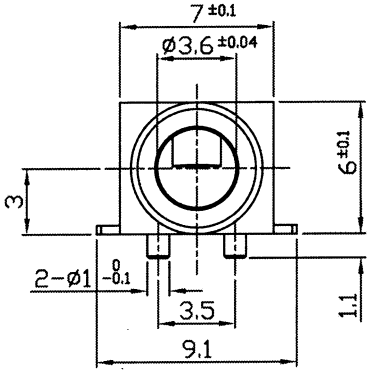
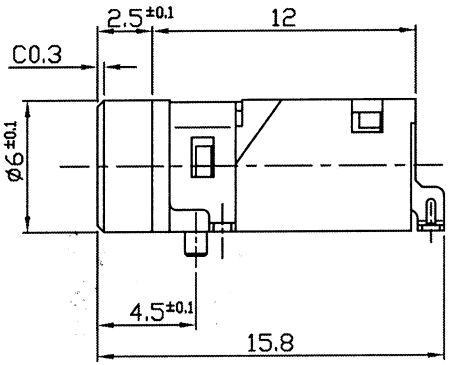
REVISION			
LTR	DESCRIPTION	DATE	APPROVED

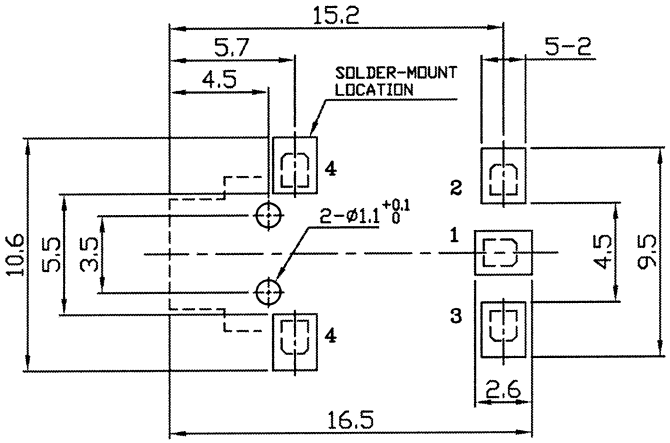


COPLANARITY FOR ALL TERMINALS

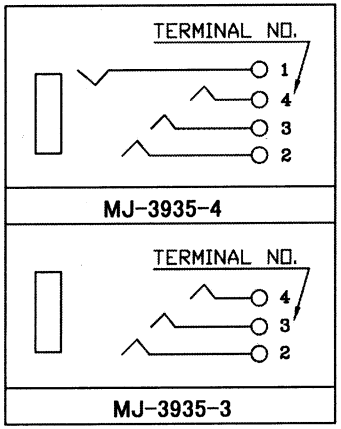




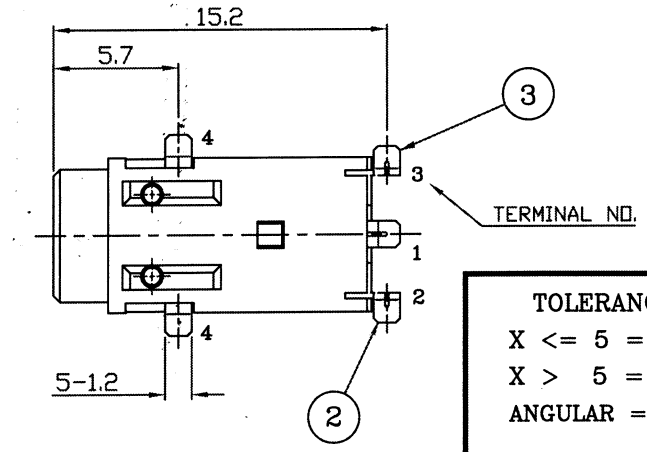




PCB HOLE & SURFACE MOUNT LAYOUT (TOLS:±0.1)



CIRCUIT DIAGRAM



5	CASING	1	PA66/GF RESIN	SEE TABLE
4	TIP SPRING 4	1	PBs 0.4t	Ag-PLATED
3	RING SPRING 3	1	PBs 0.25t	Ag-PLATED
2	SLEEVE SPRING 2	1	PBs 0.25t	Ag-PLATED
1	EARTH TERMINAL	1	PBs 0.25t	Ag-PLATED
LET	PART NAME	QTY	MATERIAL	REMARK

TOLERANCES
X <= 5 = ±0.20
X > 5 = ±0.30
ANGULAR = ±---
DO NOT SCALE DWG.

FINISHED
— — — — — —

PART NAME	DWG NO.
ø3.5 EARPHONE JACK	MJ-3935
MATL: AS SHOWN IN TABLE	SCALE: 3/1 mm
DATE: 27-11-2007	DRN / APPD.: Y.H.TSANG/K.F.IM

MARUSHIN ELECTRIC MFG. CO., LTD.