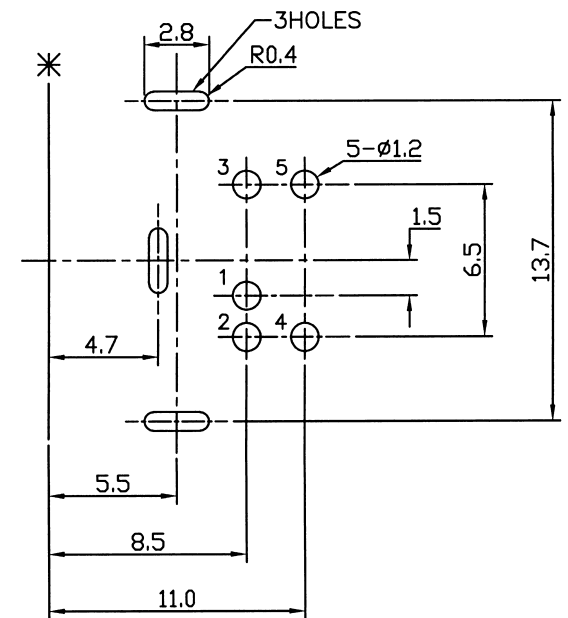
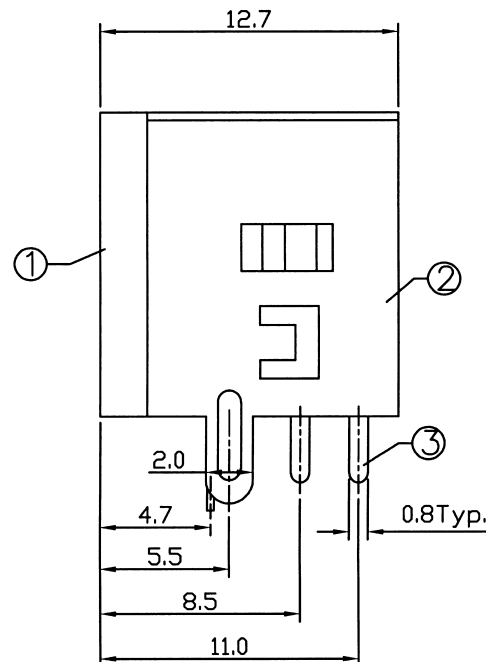
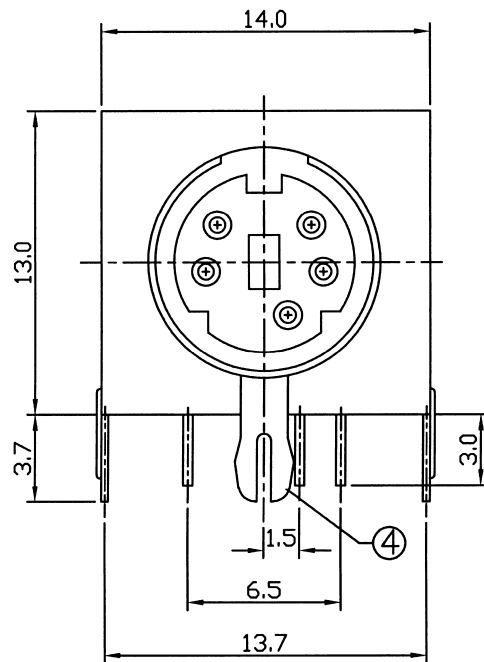
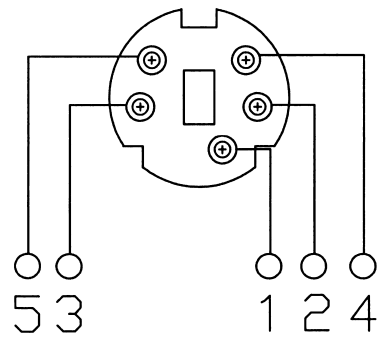


* REVISIONS				
LTR	DESCRIPTION	DATE	REVISED	APPROVED



T:1.6 Dimensions of PC board holes
Tolerance : ± 0.05 (Printed-top view)



SCHEMATIC DIAGRAM

SPECIFICATION

RATING: 100V AC 1A MAX, (12V DC 2A MAX.)
INSULATION RESISTANCE: 50M Ω MIN AT 250V DC
DIELECTRIC STRENGTH: 250V AC/1 MINUTE.

UNLESS OTHERWISE SPECIFIED, TOLERANCE ON DECIMAL: ± 0.3

4	GROUND CONTACT	1	C2680	t=0.4	Cu/Sn	PLATED
3	CONTACT FOOT	5	C5191	t=0.4	Ag	PLATED
2	COAT	1	SPCC	t=0.5	Cu/Sn	PLATED
1	HOUSING	1	PBT 4815		UL-94V0	(BLACK)
LTR	PART NAME	QTY	MATERIAL	REMARK		
DWN		DATE		3RD ANGLE PROJECTION	NAME	
CHKD		DATE			Mini din jack	
APVD		DATE			CAT. NO.	
				UNIT: mm		
MARUSHIN ELECTRIC MFG. CO., LTD.					DWN.NO.	MJ373/5B
						001